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| FORM PTO-1449 (Modified) INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary) | Docket No. 008C134US9 | Application Number 10/675,606 |
| | Applicant DENATALE et al. | |
| | Filing Date September 29, 2003 | Group Art Unit |

U.S. PATENT DOCUMENTS

| Examiner Initials | Document Number | | | | | | | Date | Name | Class | Subclass | Filing Date If Appropriate |
|-------------------|-----------------|---|---|---|---|---|---|---------|--------------|-------|----------|----------------------------|
| <i>AL</i> | 5 | 5 | 7 | 8 | 9 | 7 | 6 | 11/1996 | YAO | 333 | 262 | |
| <i>AL</i> | 5 | 8 | 8 | 0 | 9 | 2 | 1 | 03/1999 | THAM et al. | 361 | 233 | |
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FOREIGN PATENT DOCUMENTS

| | Document Number | | | | | | | Date | Country | Class | Subclass | Translation | |
|--|-----------------|--|--|--|--|--|--|------|---------|-------|----------|-------------|----|
| | | | | | | | | | | | | Yes | No |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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| <i>AL</i> | 1 | "HIGH ISOLATION CPW MEMS SHUNT SWITCHES PART 2: DESIGN", IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES, DECEMBER 1999, Muldavin et al., p. 1-4. |
| | 2 | "QUICK PROTOTYPING OF FLIP CHIP ASSEMBLY WITH MEMS", Irwin, et al., 1998, NSF CENTER FOR ADVANCED MANUFACTURING AND PACKAGING OF MICROWAVE, OPTICAL AND DIGITAL ELECTRONICS, DEPT OF MECHANICAL ENGINEERING UNIVERSITY OF COLORADO, BOULDER, CO., 8 pages. |
| | 3 | "FLIP-CHIP ASSEMBLY FOR SI-BASED RF MEMS", PROCEEDINGS OF THE 12th IEEE INTERNATIONAL CONF. ON MICROELECTROMECHANICAL SYSTEMS (MEMS 99), JAN. 1999, pp 273-278. |
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| Examiner BENNY T. LEE PRIMARY EXAMINER | | Date Considered <i>15 Oct 2004</i> |
| EXAMINER: Initial if reference is considered. Whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and then ignored. Include copy of this form with next communication to applicant. | | |